

1/13

FIG. 1A

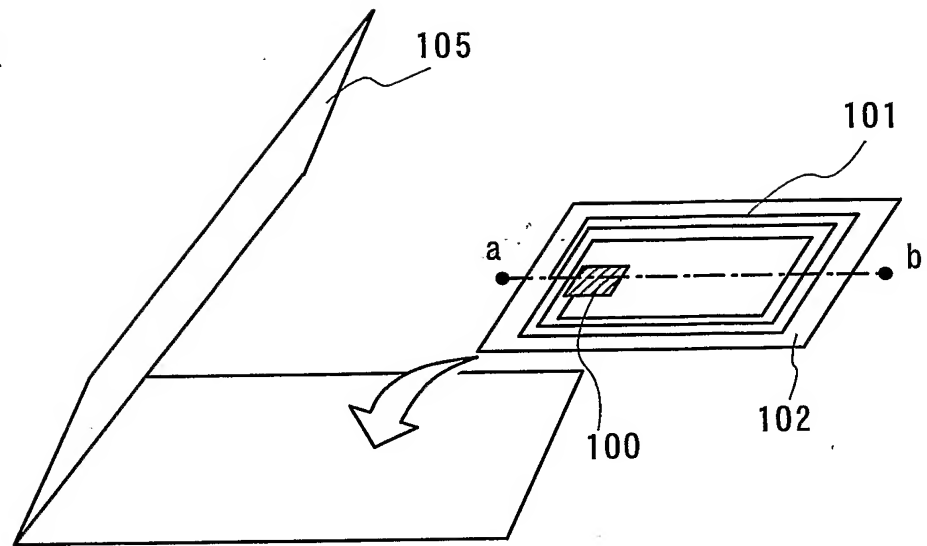


FIG. 1B

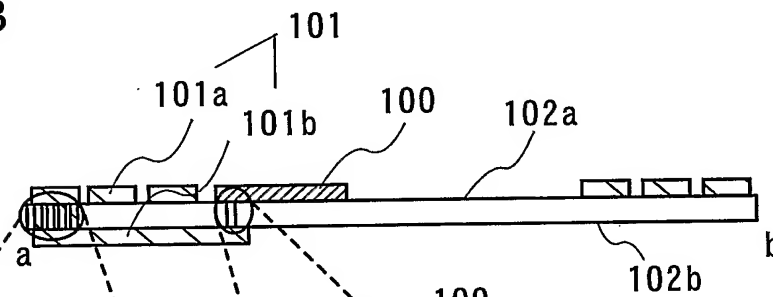


FIG. 1C

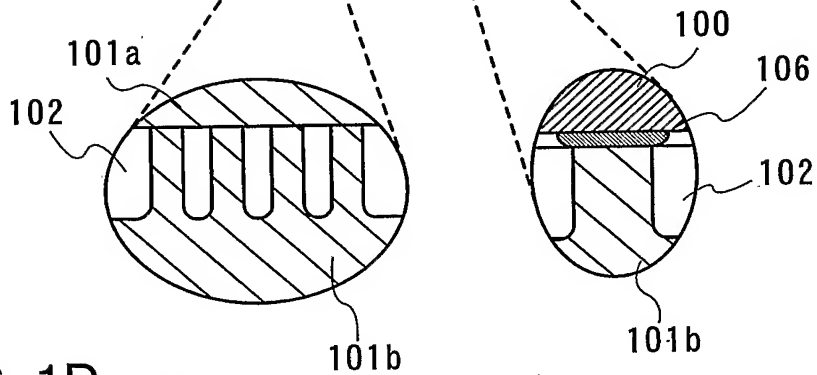
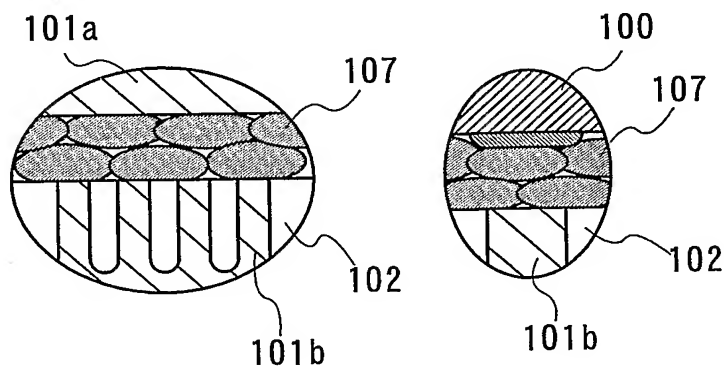


FIG. 1D



2/13

FIG. 2A

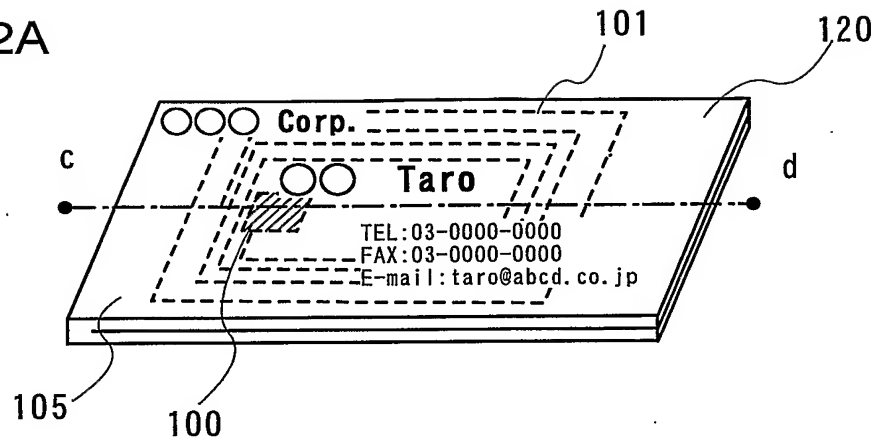
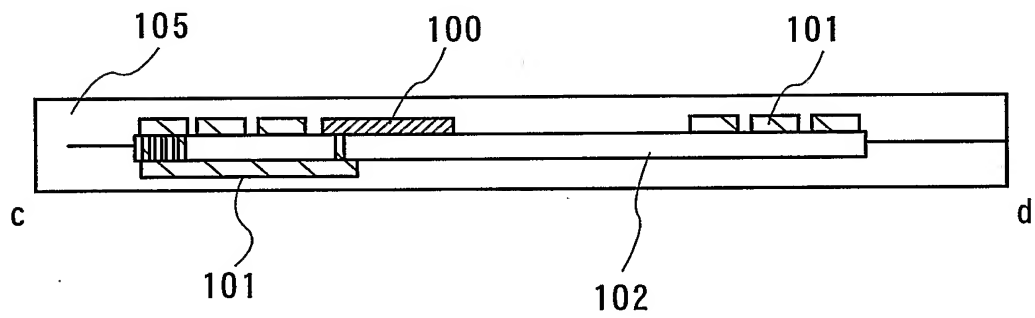
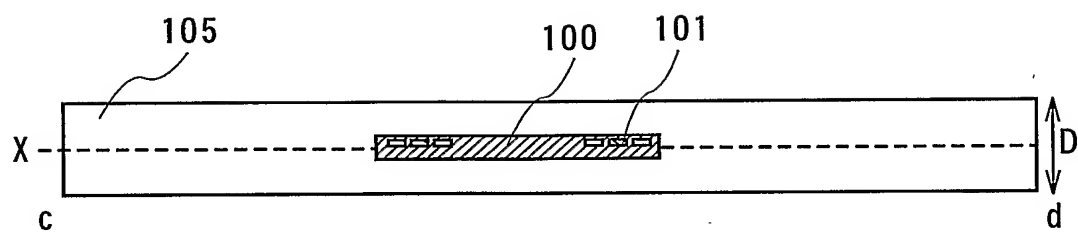


FIG. 2B



3/13

FIG. 3



4/13

FIG. 4A

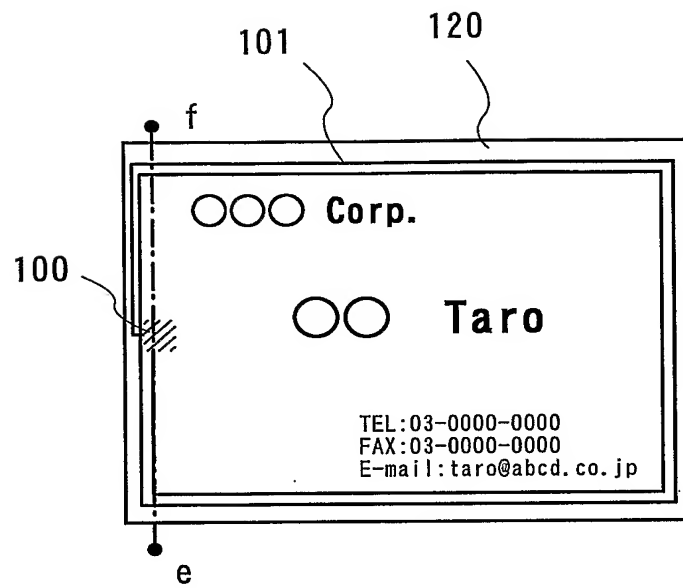
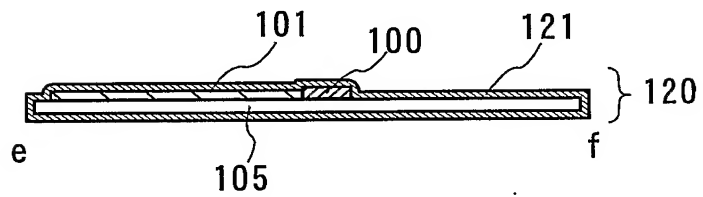


FIG. 4B



5/13

FIG. 5A

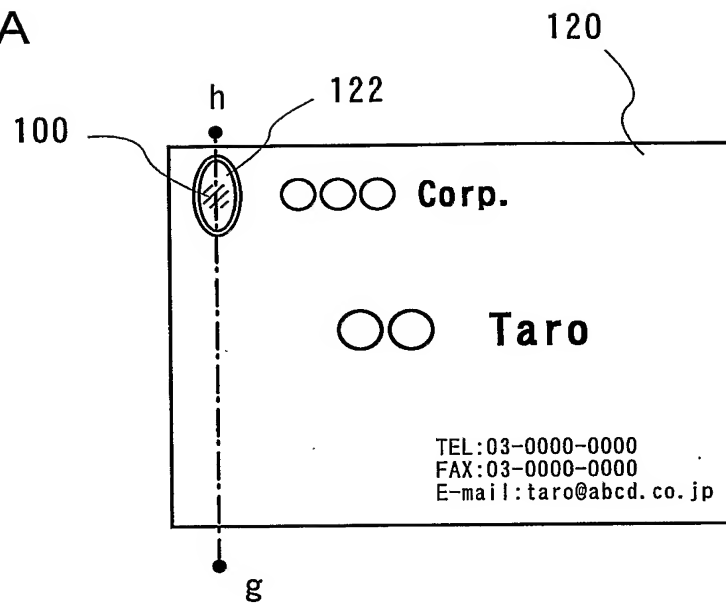
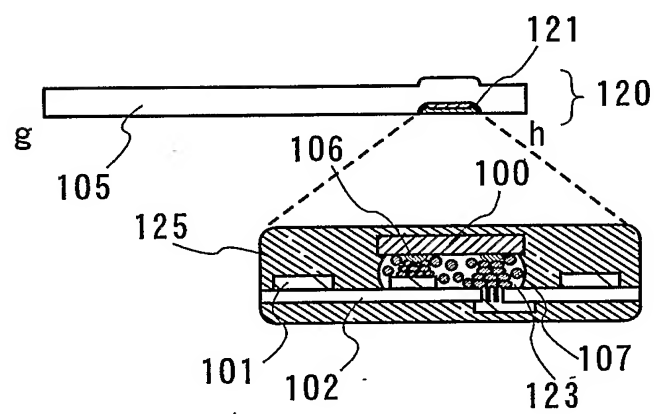


FIG. 5B



6/13

FIG. 6A

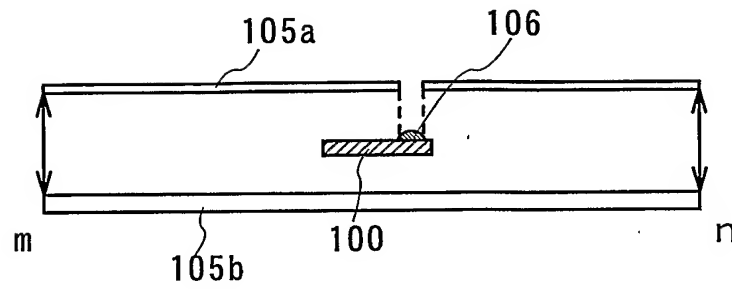


FIG. 6B

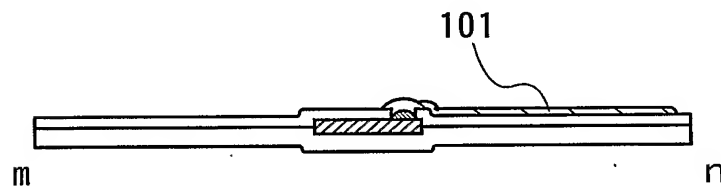
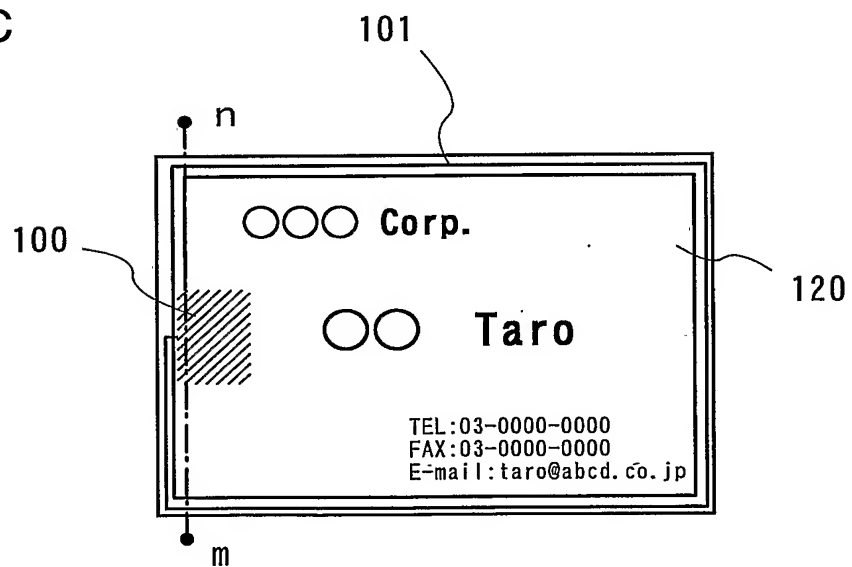


FIG. 6C



7/13

FIG. 7A

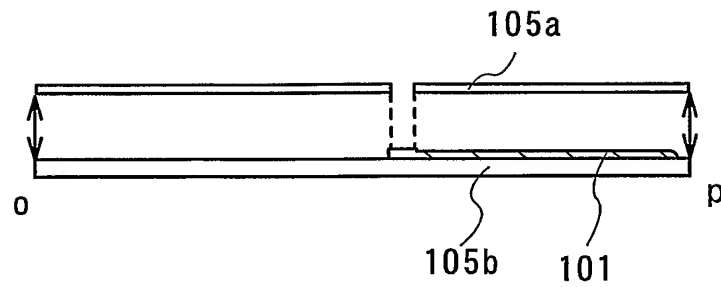


FIG. 7B

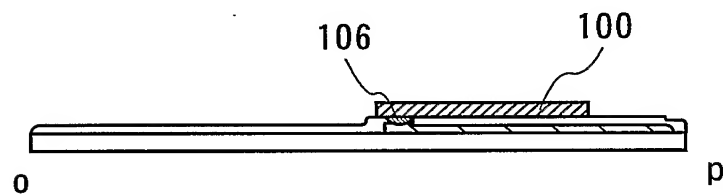


FIG. 7C

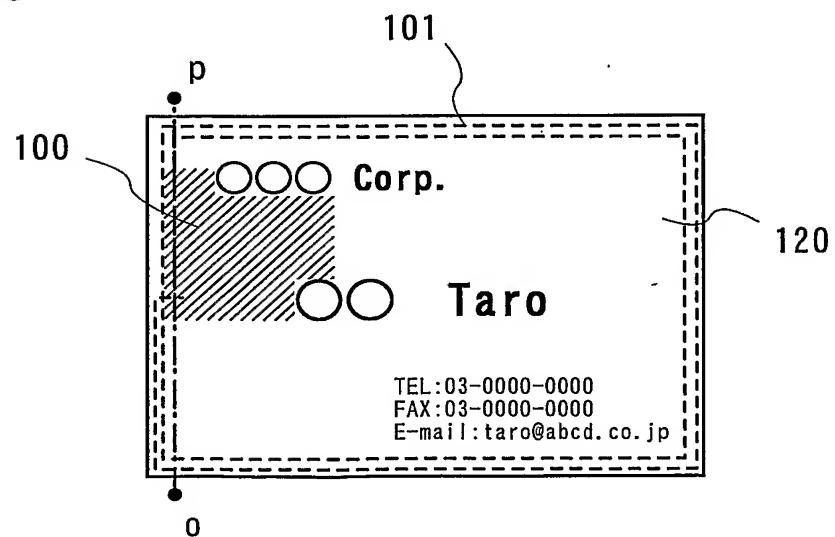


FIG. 8A

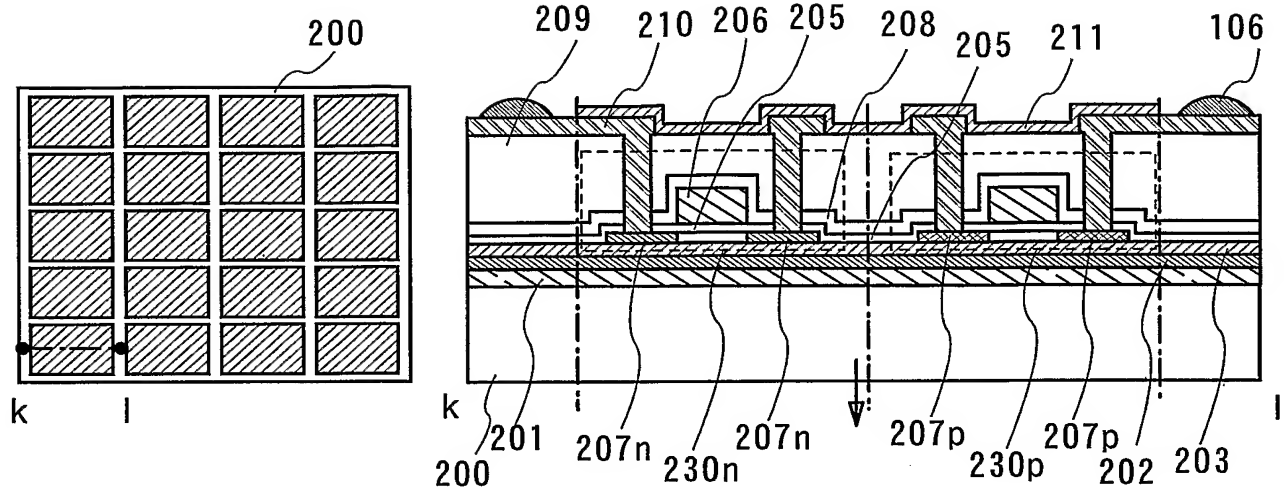


FIG. 8B

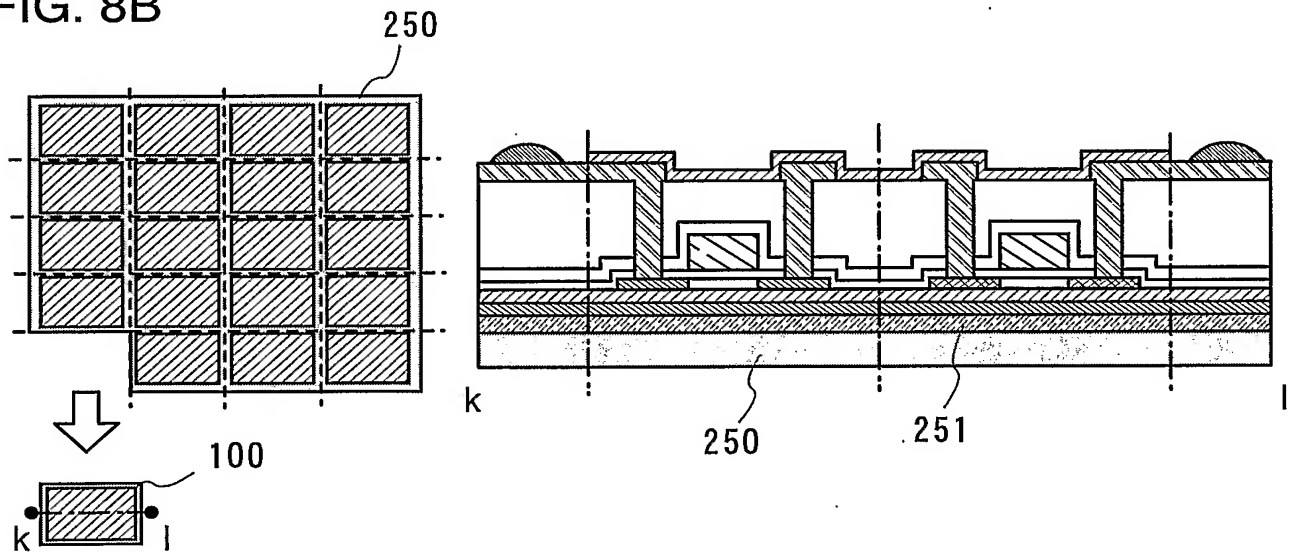
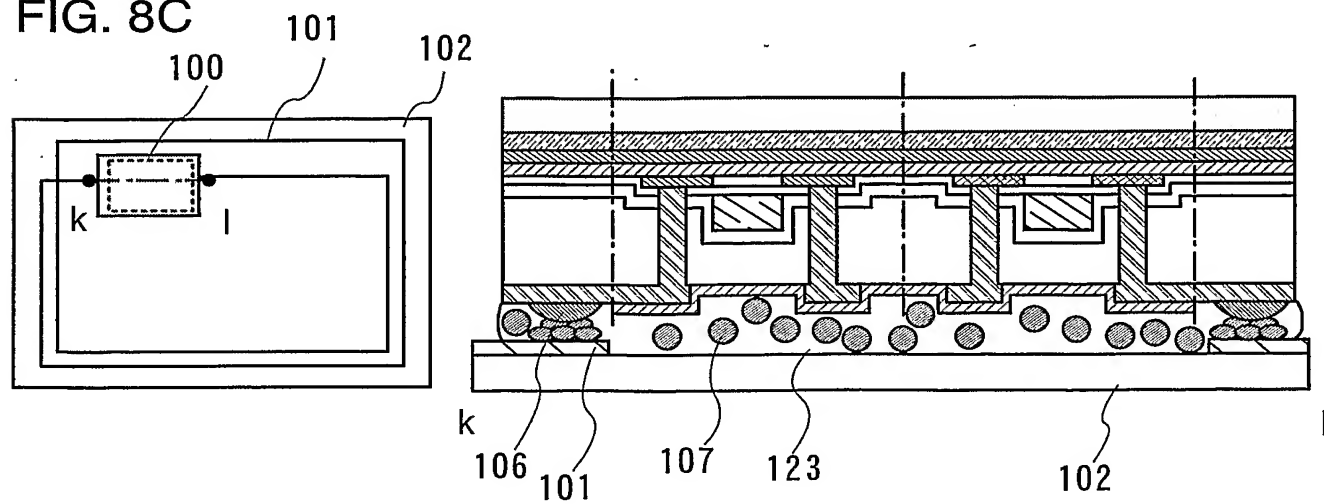


FIG. 8C



9/13

FIG.9A

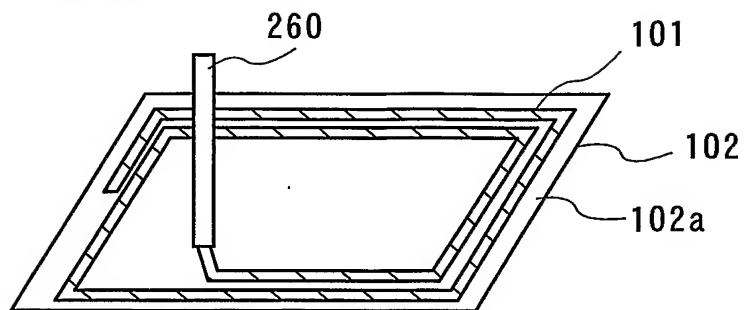


FIG.9B

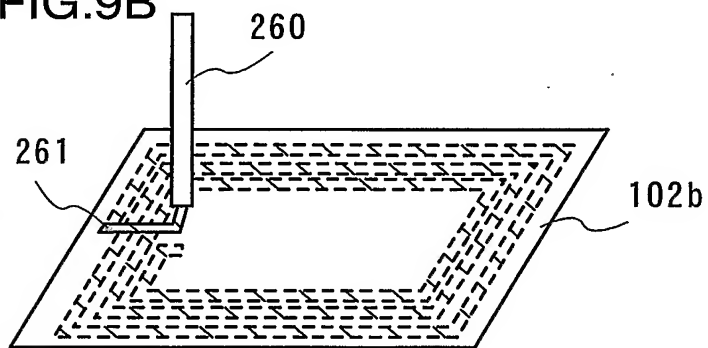
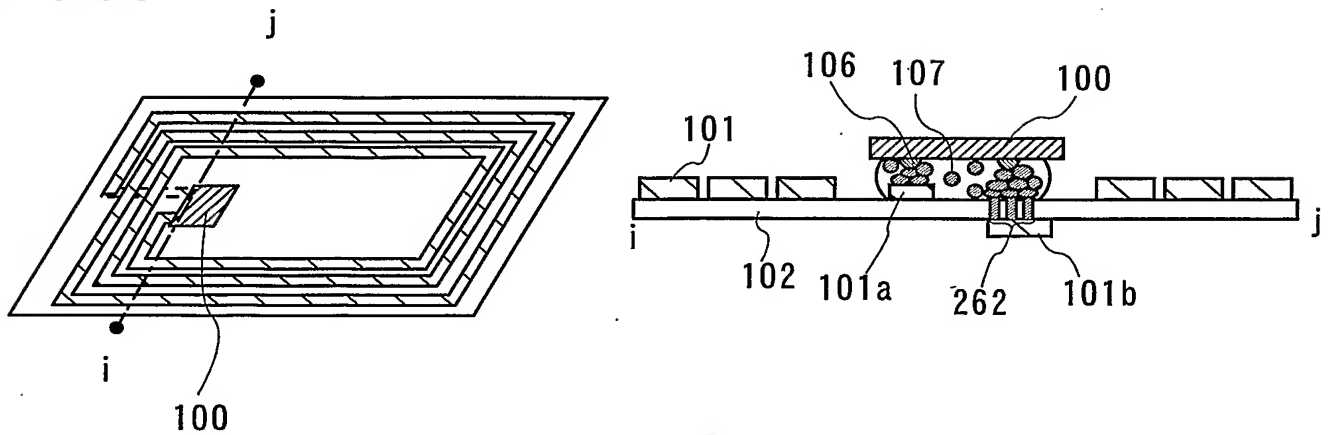


FIG.9C



10/13

FIG.10A

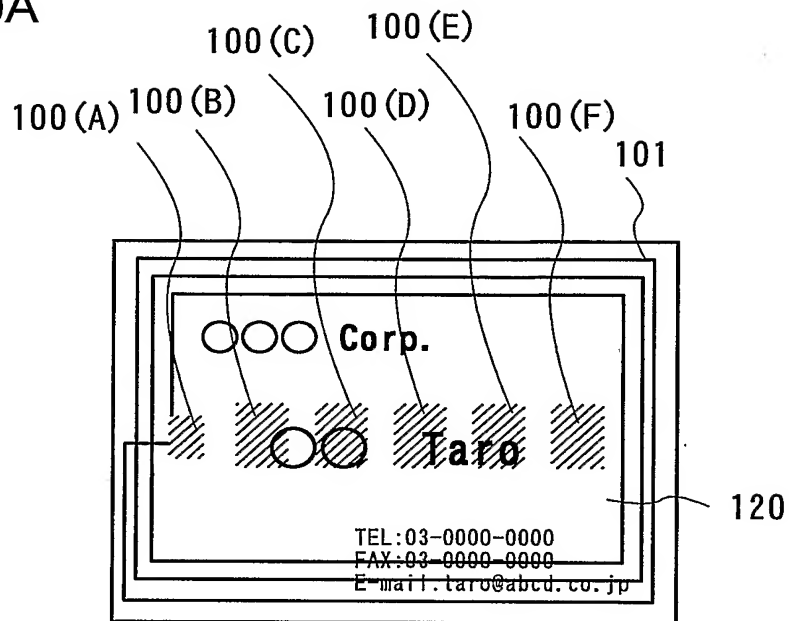
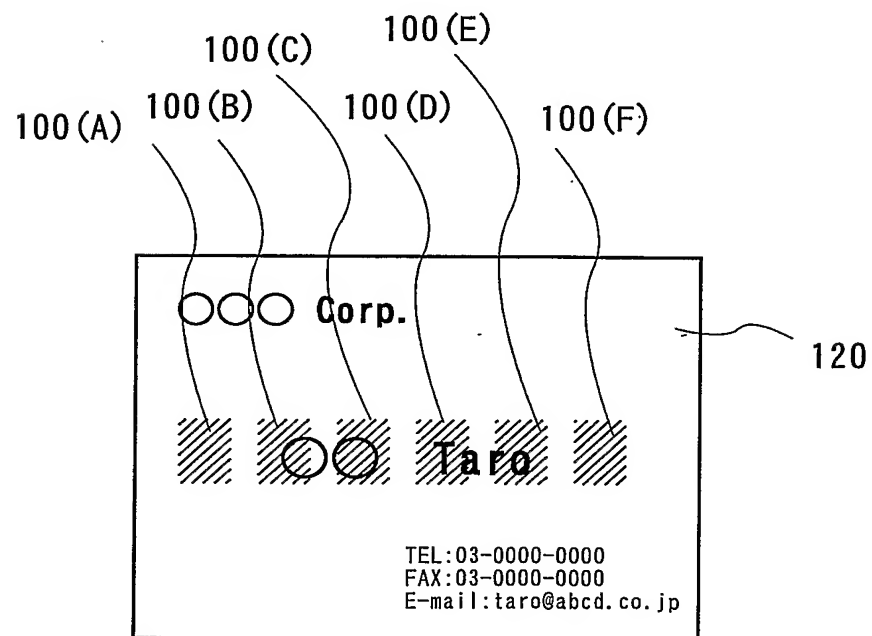


FIG.10B



11/13

FIG.11A

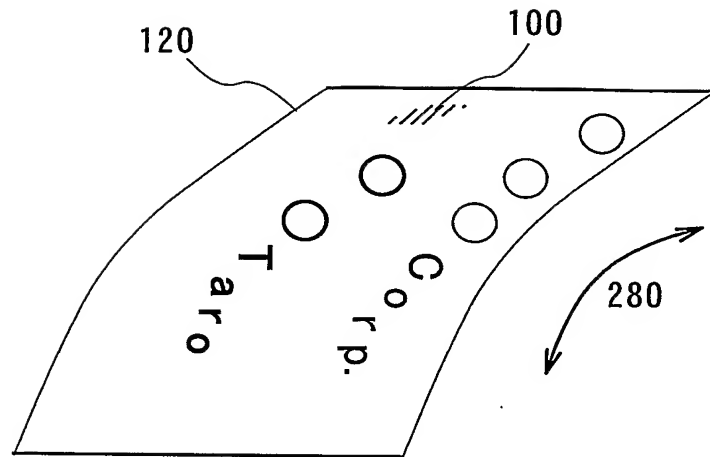


FIG.11B

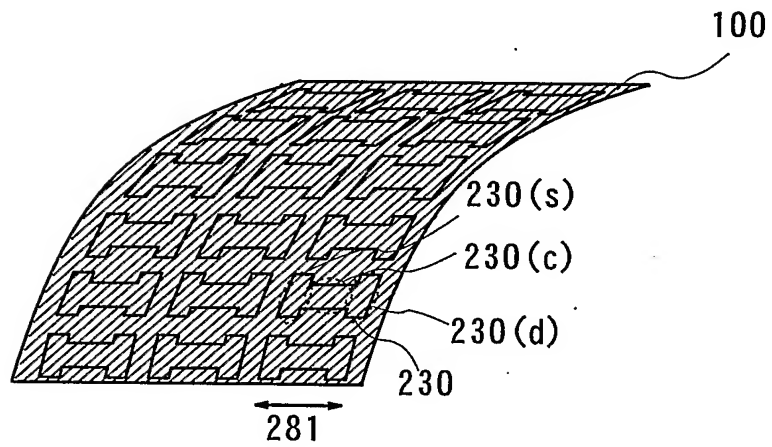
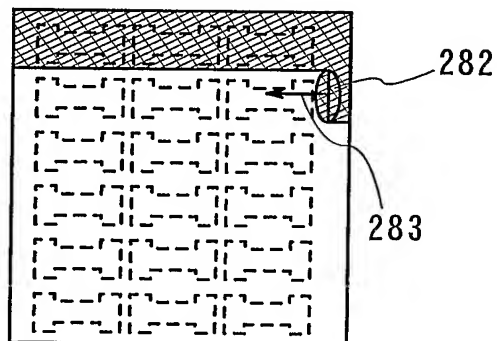


FIG.11C



12/13

FIG.12A

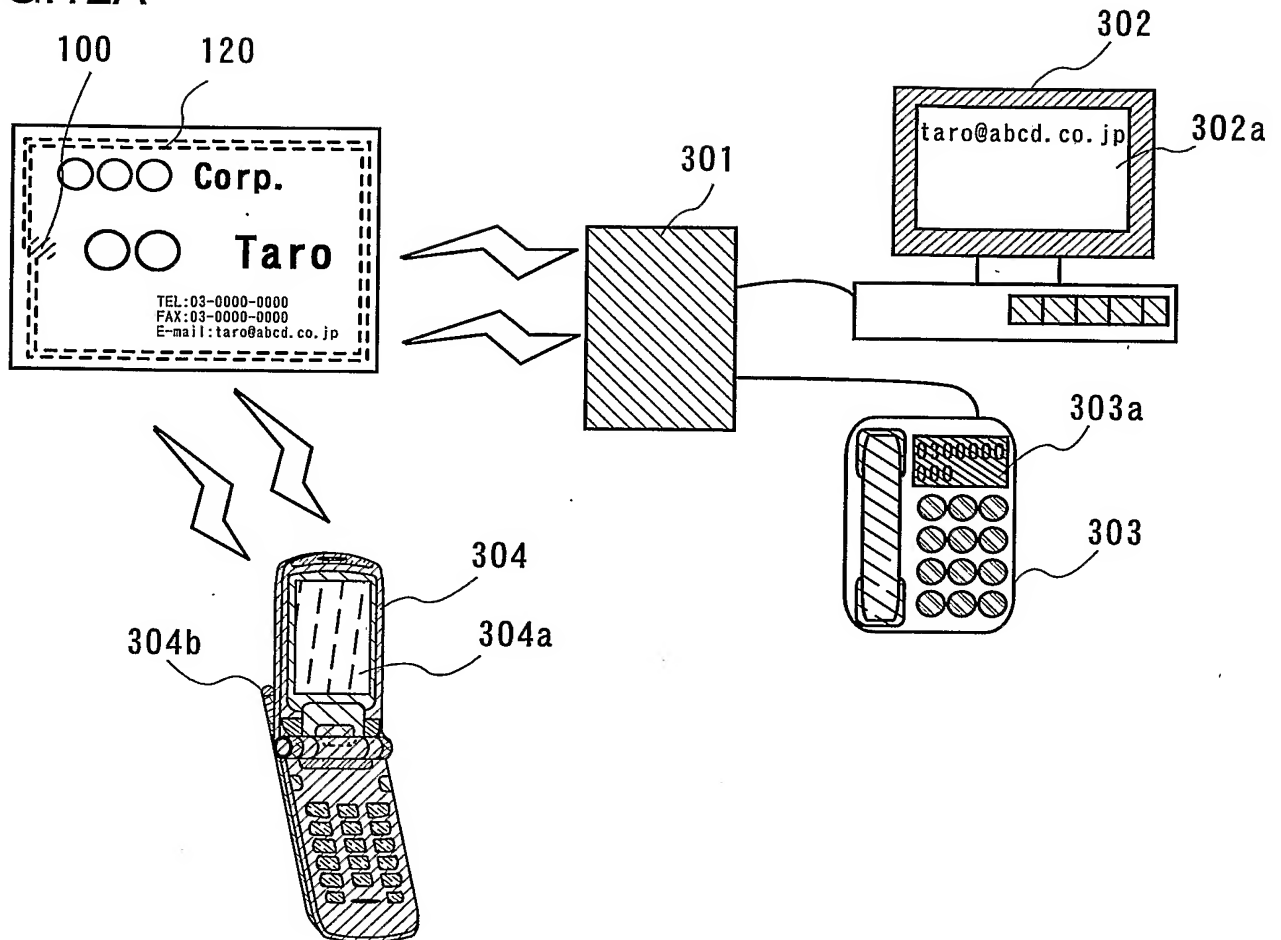
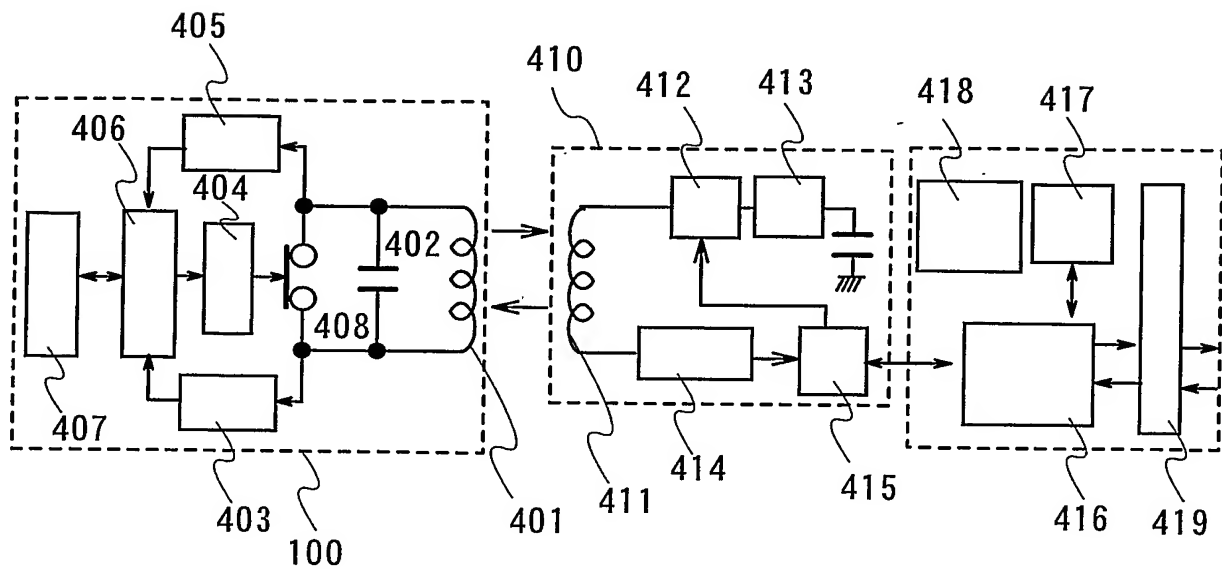


FIG.12B



13/13

EXPLANATION OF REFERENCE

100: an IDF chip, 101: antennas, 101a: an antenna, 101b: an antenna, 102: an antenna
substrate, 102a: a surface, 102b: a surface, 101b: an antenna, 101a: an antenna, 106: a
conductor bump, 107: a conductor, 105: a base member, 105a: a base member, 105b: a
5 base member, 120: a business card, 121: a resin, 122: a depression, 123: an anisotropic
conductor, 125: a resin, 200: a substrate, 201: a metal layer, 202: an insulating film, 203:
an insulating film, 207n, an impurity region, 208: an insulating film, 209: an interlayer
insulating film, 210: wirings, 211: an insulating film, 230: a thin film transistor, 230n an
n-channel thin film transistor, 230p: a p-channel thin film transistor, 230 (s) : a source
10 region, 230 (c) : a channel region, 230 (d) : a drain region, 250: a flexible substrate, 251:
an adhesive, 260: a nozzle, 262: an opening, 280: direction of the arrow, 281: a direction,
283: laser scanning direction, 282 an irradiation area (a spot), 301: a reader/writer, 302: a
personal computer, 302a: a display area, 303: a telephone, 303a: a display area, 304: a
cellular phone, 304b: an antenna, 304a: a display area, 401: an antenna coil, 402: a
15 capacitor element, 403: a demodulation circuit, 404: a modulation circuit, 405: a rectifier
circuit, 406: a microprocessor, 407: a memory, 408: a switch, 410: a reader/writer, 411:
an antenna coil, 412: a modulation circuit, 413: an oscillating means, 414: a detection
demodulation circuit, 415: a gate ASIC, 416: a microprocessor, 417: a memory, 419: a
signal interface, 418: a power supply